Physics and Engineering of CMOS Devices

Ken Uchida Department of Physical Electronics Tokyo Institute of Technology

Physics and Engineering of CMOS Devices, Ken Uchida, Tokyo Tech, May 12, 2010

1

Outline of Today's Class

- Scaling
 - Constant-Field Scaling
 - Generalized Scaling
- Short Channel Effects
- Charge Sharing Model
- Quasi-2D Model
 - DIBL (Drain-induced Barrier Lowering)
 - Electric Field in Drain Depletion Region
- Saturation Velocity

Short Channel Effects

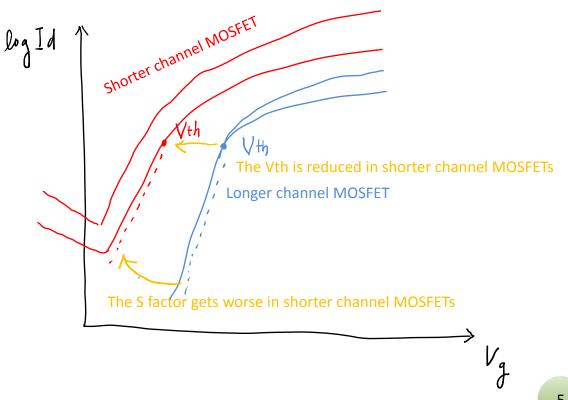
Physics and Engineering of CMOS Devices, Ken Uchida, Tokyo Tech, May 12, 2010

Short Channel Effects

- The threshold voltage $(V_{\rm th})$ of MOS transistors is lowered as the dimensions of transistors are shrunken.
- The subthreshold slope (S Factor) gets worse in shorter channel MOS transistors.
- The threshold voltage moves to a lower value with an increase in the drain voltage (DIBL).

3

Short Channel MOSFETs



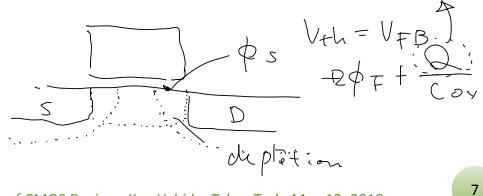
Physics and Engineering of CMOS Devices, Ken Uchida, Tokyo Tech, May 12, 2010

5

Charge Sharing Model

Charge Sharing Model

In MOS capacitors depletion charges are controlled by the gate electrode, whereas in MOS transistors the depletion charges are shared/controlled by three terminals: the gate, source, and drain electrodes. The depletion charges shared by source electrode is not controlled by the gate electrode, resulting in the $V_{\rm th}$ reduction. The $V_{\rm th}$ reduction is analyzed by charge sharing model.



Physics and Engineering of CMOS Devices, Ken Uchida, Tokyo Tech, May 12, 2010

Charge Sharing Model (2) Assumptions for charge sharing model

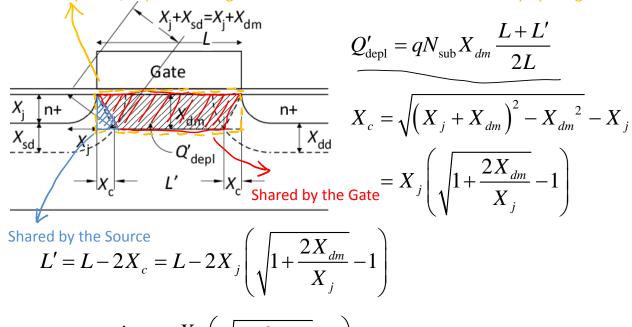
- Drain voltage (V_d) is assumed to be zero.
- The edge structure of source/drain electrodes is a quarter circle.
- The depth of the source junction is the same as that of drain junction (X_i) .
- The depletion layer width for the source/drain junction is equal to the maximum depletion layer width $(X_{sd} = X_{dd} = X_{dm})$.
- Charges at the source/drain ends are shared equally by the gate and source/drain electrodes.

 $\begin{array}{c|c} X_j + X_{sd} \\ \hline X_j + X_{sd} \\$

8

Charge Sharing Model (3)

In MOS capacitor, depletion charges in this area is assumed to be shared only by the gate.



 $F = \frac{L + L'}{2L} = 1 - \frac{X_{j}}{L} \left(\sqrt{1 + \frac{2X_{dm}}{X_{j}}} - 1 \right) \qquad Q'_{depl} = qN_{sub}X_{dm}F$

Physics and Engineering of CMOS Devices, Ken Uchida, Tokyo Tech, May 12, 2010

Charge Sharing Model (4)

$$\Delta V_{th} = \frac{Q_{\text{depl}} - Q'_{\text{depl}}}{C_{ox}} = \frac{Q_{\text{depl}}}{C_{ox}} (1 - F)$$
$$= \frac{Q_{\text{depl}}}{C_{ox}} \frac{X_{j}}{L} \left(\sqrt{1 + \frac{2X_{dm}}{X_{j}}} - 1 \right)$$

Therefore, the threshold voltage reduction is larger, if

- Oxide thickness is thicker,
- The junction depth is deeper,
- The substrate impurity concentration is smaller.

However, the charge sharing model is not suitable for analyzing MOSFET operation under strong-inversion condition.

9

Quasi-2D Model

Physics and Engineering of CMOS Devices, Ken Uchida, Tokyo Tech, May 12, 2010

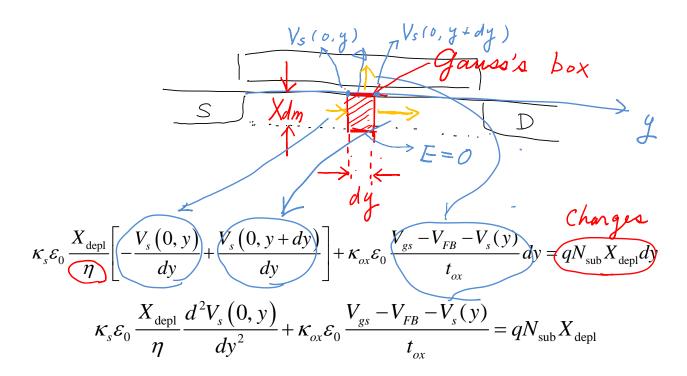
11

Quasi-2D Model (1)

The charge sharing model is suitable for analyzing MOSFET operation with small drain voltage. Since in the charge sharing model the surface potential is assumed to be constant along the channel direction, the model is inaccurate at a high drain voltage. In the quasi-2D model, the Poisson's equation is solved analytically, and thus the quasi-2D model is better to analyze $V_{\rm th}$ shift in shorter channel MOSFETs.

Z.-H. Liu et al., "Threshold Voltage Model for Deep-Submicrometer MOSFET's," *IEEE Trans. Electron Dev.*, **40** (1), p86, 1993.

Quasi-2D Model (2)



Physics and Engineering of CMOS Devices, Ken Uchida, Tokyo Tech, May 12, 2010

13

Quasi-2D Model (3)

$$\frac{d^{2}V_{s}(0,y)}{dy^{2}} + \frac{\kappa_{ox}}{\kappa_{s}} \frac{\eta}{X_{\text{depl}}t_{ox}} \left[V_{gs} - V_{FB} - V_{s}(y) - \frac{qN_{\text{sub}}X_{\text{depl}}t_{ox}}{\kappa_{ox}\varepsilon_{0}} \right] = 0$$

$$\frac{d^{2}V_{s}(0,y)}{dy^{2}} + \frac{1}{l^{2}} \left[V_{gs} - V_{th0} - V_{s}(y) \right] = 0$$

$$l = \sqrt{\frac{\kappa_{s}}{\kappa_{ox}} \frac{X_{\text{depl}}t_{ox}}{\eta}} \qquad V_{th0} = V_{FB} + \frac{qN_{\text{sub}}X_{\text{depl}}t_{ox}}{\kappa_{ox}\varepsilon_{0}}$$

$$\frac{d^{2}V_{s}(y)}{dy^{2}} - \frac{1}{l^{2}}V(y) = -\frac{1}{l^{2}} \left[V_{gs} - V_{th0} \right]$$

$$V_{s}(y) = C_{1}(y)e^{y/l} + C_{2}(y)e^{-y/l}$$